EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	53	((prevent protect) near2 (reverse adj conduct$4))	US-PGPUB; USPAT	OR	ON	2006/04/06 09:52
L2	25	1 and comparator	US-PGPUB; USPAT	OR	ON	2006/04/06 10:04
L3	50937	((low lower) adj voltage) same ((high higher) adj voltage)	US-PGPUB; USPAT	OR	ON	2006/04/06 10:05
L4	10172	3 and comparator	US-PGPUB; USPAT	OR	ON	2006/04/06 10:05
L5	1253	4 and (second adj (transistor MOS MOSFET CMOS))	US-PGPUB; USPAT	OR	ON	2006/04/06 10:07
L6	421	5 and (third adj transistor)	US-PGPUB; USPAT	OR	ON	2006/04/06 10:07
L7	304	6 and (fourth adj transistor)	US-PGPUB; USPAT	OR	ON	2006/04/06 10:07
L8	160	7 and (fifth adj transistor)	US-PGPUB; USPAT	OR	ON	2006/04/06 10:07
L10	7	"136319".ap.	US-PGPUB; USPAT	OR	ON	2006/04/06 11:15
L11	0	("2002/0167771").URPN.	USPAT	OR	ON	2006/04/06 11:27
L12	48286	(integrated adj circuit) with (drive driver (power adj (source supply)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/06 11:28
L13	6264	((integrated adj circuit) with (drive driver (power adj (source supply)))).ti.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/06 11:28
L14	187	13 and (((high higher) adj voltage) or high-voltage) same (((low lower) adj voltage) or low-voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/06 11:36
L15	85	((two adj stage) or (dual adj stage) or two-stage or dual-stage) adj driver	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/06 11:41

L16	24	15 and (second adj (transistor mosfet fet))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/06 11:37
L17	66	(second adj voltage) with driver with (integrated adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/06 11:47
L18	0	(second adj voltage adj rail) with driver with (integrated adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/06 11:41
L19	489	307/43.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/06 11:48
L20	887	307/43,51,75.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/06 11:50
L21	21	20 and ((reverse adj (current conduct conduct$4)) with (prevent protect prevent$4 protect$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/06 14:52
L22	2790	307/51,71,80,85,87.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/06 15:05
L23	241	307/51.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/06 15:06

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L24	137	307/71.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/06 15:10
L25	351	307/80.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/06 15:11
L26	317	307/85.ccls.	US-PGPUB; USPAT	OR	ON	2006/04/06 15:21
L27	892	307/87.ccls.	US-PGPUB; USPAT	OR	ON	2006/04/06 15:31
L28	172	27 and (transistor mos mosfet)	US-PGPUB; USPAT	OR	ON	2006/04/06 15:32
L29	83	28 and comparator	US-PGPUB; USPAT	OR	ON	2006/04/06 16:11
L30	4	"706467".ap.	US-PGPUB; USPAT	OR	ON	2006/04/06 16:41
L31	7	"136319".ap.	US-PGPUB; USPAT	OR	ON	2006/04/06 16:41
L32	3	"346530".ap.	US-PGPUB; USPAT	OR	ON	2006/04/06 16:41
S1	293	361/82.ccls.	US-PGPUB; USPAT	OR	ON	2006/04/06 09:52
S2	310	361/84.ccls.	US-PGPUB; USPAT	OR	ON	2006/04/06 11:15
S3	0	("2005/0099748").URPN.	USPAT	OR	ON	2006/04/05 16:04